



EXPEDITED PROCEDURE – EXAMINING GROUP 2883

S/N 10/732,929

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Tieyu Zheng	Examiner:	Michael P. Mooney
Serial No.:	10/732,929	Group Art Unit:	2883
Filed:	December 11, 2003	Docket No.:	884.G22US1
Title:	METHOD AND APPARATUS FOR MANUFACTURING A TRANSISTOR- OUTLINE (TO) CAN HAVING A CERAMIC HEADER		
Assignee:	Intel Corporation	Customer Number:	21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

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This responds to the Final Office Action dated September 6, 2006, please amend the application as follows: